

Amp 205842-1  
(D connector)

PIN	FUNCTION	WIRE
1	PRE-EMPTOR 5 ACTIVE	175
2	SYS COMMAND OFFSET BIT 3 OP	176
3	SPLIT DEMAND	177
4	SYS COMMAND COORD SYNC IP	178
5	CROSS STREET SYNC	179
6	SYS COMMAND CYCLE BIT 3 IP	180
7		181
8	NIC SPECIAL FUNCTION 2	182
9	SYS COM SPLIT BIT 2 IP (EAB-4)	183
10	SYS COM OFFSET BIT 2 IP (EAB-1)	184
11	NIC SPECIAL FUNCTION 4/	185
12	SYS COM OFFSET BIT 1 IP (EAB-0)	186
13	EXPANDED DETECTOR 8	187
14	TIME REST	188
15	PRE-EMPTOR FLASH CONTROL	190
16	SYS COM SPLIT BIT 1 IP (EAB-2)	191
17	VEHICLE DETECTOR 21	192
18	VEHICLE DETECTOR 24	193
19	TEST INPUT E	194
20	TEST INPUT C	195
21	SYS COM SPLIT BIT 1 OP	196
22	PRE-EMPTOR 3 ACTIVE	197
23	PRE-EMPTOR 1 ACTIVE	198
24	NIC SPECIAL FUNCTION 3	199
25	SYS COM CYCLE BIT 1 IP	200
26	CO-ORD FREE	201
27	CO-ORD STATUS	202
28	NIC SPECIAL FUNCTION 1	203
29	SYS COM CYCLE BIT 3 OP	204
30	EXPANDED DETECTOR 5	205
31	VEHICLE DETECTOR 23	206
32	PRE-EMPTOR 2 ACTIVE	207
33	SYS COM OFFSET BIT 1 OP	208
34	PRE-EMPTOR 4 ACTIVE	209
35	SYS COM CYCLE BIT 2 IP	210
36	SYS COM OFFSET BIT 3 IP (EAB-2)	211
37	TEST INPUT D	212
38	DUAL CO-ORD	213
39	EXPANDED DETECTOR 6	214
40	EXPANDED DETECTOR 7	215
41	SPARE OUTPUT 4	216
42	SYS COM OFFSET BIT 2 OP	217
43	SYS COM CYCLE BIT 1 OP	218
44	SYS COM CYCLE BIT 2 OP	219
45	SPARE OUTPUT 5	220
46	SYS COM SPLIT BIT 2 OP	221
47	VEHICLE DETECTOR 22	222
48	PRE-EMPTOR 6 ACTIVE	223
49	PRE-EMPTOR CALL 2	224
50	PRE-EMPTOR CALL 3 (BUS 1)	225
51	SPARE OUTPUT 6	226
52	SPARE OUTPUT 7	227
53	SYS COM SYNC OUT	228
54	SPARE OUTPUT 8	229
55	PRE-EMPTOR CALL 4 (BUS 2)	230
56	PRE-EMPTOR CALL 5 (BUS 3)	231
57	PRE-EMPTOR CALL 1	232
58	CMU STOP TIME (CON FLASH)	233
59	PRE-EMPT CMU INTERLOCK	234
60	AUTOMATIC FLASH	235
61	PRE-EMPTOR CALL 6 (BUS 4)	236
62		237
63		238

Amp 205843-1  
(MSD connector)

WIRE	PIN	FUNCTION
		FLASH
176	2	OFFSET 1
177	3	INTERCONNECT COMMON
178	4	USER DEFINED IP 6
179	5	OFFSET 2
180	6	OFFSET 3
181	7	CYCLE 2
182	8	USER DEFINED IP 7
183	9	USER DEFINED IP 8
184	10	CALL TO FREE OPERATION
185	11	CALL TO WEEK 10
186	12	CYCLE 3
187	13	SPLIT 2
	14	SPLIT 3
190	15	LAG $\phi$ 1
191	16	LAG $\phi$ 3
		LAG $\phi$ 5
		DIMMING
194	19	DUAL ENTRY
195	20	SYSTEM IN
196	21	LAG $\phi$ 7
		SPARE IP *
		SPARE IP *
C-m	147	24 #13 DETECTOR INPUT
B-i	88	25 #9 DETECTOR INPUT
B-h	87	26 #10 DETECTOR INPUT
B-U	74	27 #11 DETECTOR INPUT
	203	28 POLARIZING KEY
B-S	72	29 #12 DETECTOR INPUT
C-p	149	30 #14 DETECTOR INPUT
C-EE	164	31 #15 DETECTOR INPUT
C-X	133	32 #16 DETECTOR INPUT
	208	33 SGO/CONDITIONAL SERV
D-56	231	34 PRE-EMPT 5 IN
D-23	198	35 PRE-EMPT 1 OUT
D-32	207	36 PRE-EMPT 2 OUT
	212	37 INTERCONNECT INH
D-14	188	38 TIME CLOCK SYNC
	214	39 SYNC INH
D-57	232	40 PRE-EMPT 1 IN
D-49	224	41 PRE-EMPT 2 IN
D-50	225	42 PRE-EMPT 3 IN
D-22	197	43 PRE-EMPT 3 OUT
		44 POLARIZING KEY
D-34	209	45 PRE-EMPT 4 OUT
D-1	175	46 PRE-EMPT 5 OUT
		SYSTEM OUT
	223	48 PRE-EMPT 6 OUT
D-55	230	49 PRE-EMPT 4 IN
		CKT 9
	226	51 CKT 10
	227	52 CKT 11
	228	53 CKT 12
	229	54 CKT 13 (SYSTEM)
		CKT 8 (FLASH / AW8)
		CKT 3 (OFFSET 1 / AW3)
		CKT 4 (OFFSET 2 / AW4)
233	58	CKT 5 (OFFSET 3 / AW5)
234	59	CKT 1 (CYCLE 2 / AW1)
235	60	CKT 2 (CYCLE 3 / AW2)
		CKT 6 (SPLIT 2 / AW6)
		CKT 7 (SPLIT 3 / AW7)
D-61	236	63 PRE-EMPT 6 IN

DEFAULT PIN TO PIN WIRING  
BETWEEN CONNECTORS D & MSD

MSD-46

MSD-38

DB-15-5

DB-15-8

MSD-43

MSD-35

DB-15-7

MSD-36

MSD-45

DB-15-6

MSD-41

MSD-42

MSD-49

MSD-34

MSD-40

MSD-63

No.	Revision	Date
DRAWING ISSUED		DEC03

TRAFFIC CONTROLLER ASSEMBLY  
ADAPTOR HARNESS



BRITISH  
COLUMBIA

Ministry of  
Transportation

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DRAWING No.  
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